

About us

Company: ACTIVE SEMI VIETNAM LIMITED

Address: Floor 9th, 3D Building, Duy Tan Street, Dich Vong Hau Ward, Cau Giay district, Hanoi city

Website: www.active-semi.com

Active-Semi Vietnam is located in Hanoi city and we are a part of Active-semi Inc., that found in 2004 in Silicon Valley, California, USA by the semiconductor veterans who had worked for many years from TI, Maxim... We are around the world with many sale offices in US, Euro, Japan, Korea, Shanghai, Shenzhen and R&D centers in Vietnam, Shanghai and headquarter in Dallas, USA. We have shipped over 1.5 billion ICs in a broad range of application from power application controller, AC/DC, DC/DC, PMU and you can find us from many end user equipment such as Drone, SSD, Air Conditioner, Ceiling Fan, Tablet, Power Bank, Car Charger, Cellphone Adapter, Wireless Charger...

Active-Semi Vietnam proud of being a pioneer in Integrated Circuit and Analogue IC design in Vietnam since 2004 with many generations of engineer who has grown up and became world class engineers especially in power management IC with many successful commercial IC for our customers. Working with us you have chances to work with the experts with many years of experienced about analog, mixed mode signal IC and working oversea in Shanghai or USA.

Layout Engineer (*fresh graduate students are welcome to apply*)

Title:	Layout Engineer
Education:	Bachelor's Degree
Quantity:	3
Minimum Experience:	1 year (<i>fresh graduate students are welcome to apply</i>)
Hours:	Full Time
Length of Employment:	Regular
Location:	Hanoi

JD:

Work as a team with other layout engineers and designers, local and/or globally to complete full chip projects. Responsibilities include all aspects of IC mask design from creating block layout to full chip floor plans, planning schedules, hookup, and verification to tape-out.

- ✓ Being able to understand array layout specificity
- ✓ The ability to read and understand complex designs
- ✓ Good understanding of analog/mixed signal CMOS circuit design concepts.
- ✓ Proficient to read and layout according to IC design rule manual

Qualification and Requirements

- ✓ Bachelor's Degree in Electrical, Electronic and Telecommunication or related field.
- ✓ A strong foundation in electronic circuit layout and analysis.
- ✓ Ability to work well in a team environment and able to work independently at high pressure.
- ✓ Good written and verbal communication skills, good at reading and writing in English and presentation is advantage

Experience :

- ✓ Experience in Analog IC layout design.
- ✓ Knowledge of EDA Layout Tools (Cadence Virtuoso, Assura, PVS....)
- ✓ Good at Microsoft Office (Word, Excel, Power point) .

- All experience candidates and fresh graduate candidates students are welcome to apply CV.

Benefit:

At Active-Semi, you will work in an international and professional working environment with experts about analogue IC design and attractive offers:

- ✓ Have attractive, competitive salary and incentives
- ✓ Have extra health care insurance
- ✓ Lunch allowance
- ✓ Have high Social, Health and Unemployment insurances
- ✓ Annual health check
- ✓ Annual company outing
- ✓ Annual leave is added one day for every one year service
- ✓ Birthday gift, wedding gift, new born gift...
- ✓ Sport club/activities
- ✓ Other team building activities

Contact:

Your CV will be sent to: Ms Ellen Pham from 15 May 2017 to 30 Jun 2017

Tel: 04 3768720/ ext 169

Mobile: 0977409306

Email: ellenpham@active-semi.com

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